Annexure M7

Scope of work/Works Information/Employers Requirements- 3 ITEM LINES

Item	Material	Description	Required Quantity	Unit Price
1.	**0191044**	**TESTER:SECONDARY INJECTION TEST SET - HARDWARE – SUPPLY MATERIAL AND ACCESSORIES** **Power Supply** Nominal Voltage 230 V, Single Phase Permissible Voltage Range 90 V - 260 V Nominal Frequency 50 Hz Permissible Frequency Range 45 Hz - 65 Hz Power Consumption Max 3450VA at 230VAC Rated Current 15A at 230VAC	**1**	

		Connection Standard AC sockets: Connector (SANS 60320-1, C13) and Inlet (SANS 60320-1, C14) **Dimensions and Weight** Volume max. 30 litre Weight max. 20 kg **Environment** Operating Temperature 0 ℃ ... 50 ℃ Storage Temperature -25 ℃ ... 70 ℃ Humidity 5 % ... 95 % r.h. (non-condensing)		

		Certificates Vibration IEC 60068-2-6 Shock IEC 60068-2-27 CE Yes EMC The product adheres to the electromagnetic compatibility (EMC) Directive 2004 / 108 / EC (CE conform) EMC - Emissions IEC 61326-1 Class A; IEC61000-6-4; IEC61000-3-2/3; FCC Subpart B of Part 15 Class A EMC - Immunity IEC 61326-1; IEC 61000-6-2; IEC 61000-4-2/3/4/5/6/11 **Safety** The product adheres to the low voltage Directive 2006 / 95 / EC (CE conform)		

		IEC 61010-1; Insulation of PC and SELF Interfaces complies with EN 60950-1 **Independent Test Laboratory** Copies of test certificates from independent test laboratories proving compliance with the above standards are to be supplied together with the tender **Hardware** **General** All functions should be combined in one hardware unit The unit shall be of a robust and sturdy construction PC card design: Wire jumpers on a single printed circuit board are not permissible Electronic components: No potentiometers are allowed No moving elements or elements that are susceptible to damage, i.e. control elements or displays on the face plate are permissible **Output Amplifiers** The amplifier stages are to be fully electronic, i.e. not via transformer All current amplifiers to be fully protected and proof against any open-circuit-, overload-, overburden- and over-temperature- condition. Any such condition is to be immediately displayed in all active software modules. Except for an over-temperature condition an automatic shut down of the amplifiers is NOT permissible		

		All voltage amplifiers to be fully protected and proof against any short-circuit-, overload-, overburden- and over-temperature- condition. Any such condition is to be immediately displayed in all active software modules. Except for an over-temperature condition an automatic shut down of the amplifiers is NOT permissible All amplifiers to use linear amplification elements and to be dc-coupled Ability to generate dc and ac signals The amplifiers, low-level outputs, the measurement inputs and the main power supply to be galvanically isolated from each other and earth (2kV insulation voltage) **Voltage Amplifiers** Setting range 4x 0..300Vrms (L-N), 3x 0..520Vrms (L-L) Single phase operation 1x 0..600Vrms (L-L) Output power 3x 100VA at 100..300V (L-N) or 1x 200VA at 100..300V (L-N) or 1x 275VA at 200..600V (L-L) Maximum load current		

		3x 1Arms; 1x 2Arms Accuracy error < 0.03% of reading (0..300V) + 0.01% of range THD+N < 0.015% Phase error < 0.05° Connection 4mm Banana sockets amplifier combination plug (3xV and 3xI) **Current Amplifiers** Setting range 6x 0..32Arms 3x 0..64Arms Single phase operation 1x 0..128Arms Output power		

		6x 430VA at 25A 3x 800VA at 50A 1x 1000VA at 80A (LL-LN) or 1x1740VA at 25A (L-L-L-L) Maximum compliance voltage 6x 35Vpk 1x 140Vpk Accuracy error < 0.05% of reading (0..32A) + 0.02% or range THD+N < 0.05% Phase error < 0.05° amplifier combination plug (3xV and 3xI) Independent amplifiers- The six current amplifiers are to be independent from the four voltage amplifiers. The neutrals between the two current output groups are to be galvanically isolated (up to 2kV) to allow for series connection of the two groups in order to boost the compliance voltage **Connection**		

		4mm Banana sockets **Low-level signal Generators** 6 additional analog low level signals to control external amplifiers or to test relays with low-level input, e.g. from Rogowski coils, linear voltage or linear current sensors must be provided. To allow for expandable options from 6 to 12 outputs. Full simulation of Rogowski coil signal (i.e. first order differential of signal) to be performed in hardware. Testing the Sub Synchronous Resonance (SSR) numerical relays at Koeberg and for the injection of White Noise signals into the AVR for grid compliance testing. Together with 10 internally used signal generators the system should provide 16 independent signal channels in total Output settings range 0 ... 10 Vpk Accuracy <0.025% Overload Protection- Yes The quantities displayed in the software must be saleable for primary or secondary voltages (or currents). Should be able to couple to other auxiliary devices or possible synchronising function.		

		Signal Generation All outputs to be continuously and independently adjustable in amplitude, phase (0 to +/- 360°) and frequency. Able to generate continuous sine waves with a frequency between 10 and 1kHz and to generate transient files with a bandwidth from dc up to 3 kHz. Frequency error to be less than 0.5 ppm. Phase error to be less than 0.02° **Binary Inputs** Number of inputs- 10 in at five galvanic isolated groups (2kV insulation voltage) Mode/Trigger criteria- Pick-up and drop-out of potential-free contacts or dc voltages of up to 600Vdc. Trigger levels to be adjustable Max. Input Voltage- 600V Max. error < 100 µs Counting function- Inputs capable of counting number of pulses up to 3kHz. Connection- 4mm Banana sockets		

		Analogue Inputs for Measurement Number- 10 in five galvanic isolated groups (2KV insulation voltage) Input ranges- 100mV, 1V, 10V, 100V, 600V Accuracy <0.06% Bandwidth- dc..10kHz Sampling frequency- 3kHz to 28kHz Overload protection- Yes Recording buffer- 300s for 1channel at 3kHz, 3.5s for 10 channels at 28kHz Connection- 4mm Banana sockets **Analog low level measuring inputs for transducer testing** Direct current range Range 1: 0 … ±1 mA Range 2: 0 ... ±20 mA Direct voltage range 0 ... ±10 V Max. error		

		< 0.003% Connection 4mm Banana sockets **Auxiliary DC supply to power test objects** Range Range 1: 0 … 264 Vdc, 0.2A Range 2: 0…132 Vdc, 0.4A Range 3: 0…66 Vdc, 0.8A Power max. 50W Max. error < 2% **Binary output contacts** Number- Minimum 4 Breaking capacity- 300V, 8A, 2000VA or 50W Type- Dry contacts that can be used to switch ac or dc		

		Time Synchronization The test set should be able to synchronise to a GPS clock via a 1pps or IRIG B signal. Timing accuracy for 1pps / IRIG-B: 1 µs. Maximum distance between GPS receiver and test equipment for 1pps signal: 40m. Test set needs to be synchronise to the IEEE 1588 Precision Time Protocol (PTP) via Ethernet. Timing accuracy for PTP: 100ns. Maximum distance between GPS receiver and test equipment for PTP: 2000m. IEEE 1588 / PTP clock to be powered over Ethernet (PoE), i.e. no separate power supply should be necessary. **Interface to PC / Laptop** Interface to the IBM compatible PC via Ethernet interface OR USB 10/100Mbit/s copper (autosensing, auto crossover) via RJ45 connector Note: Centronics parallel port (Lpt) and/or serial RS232 ports are not permissible as modern laptops do not provide such ports. Two Ethernet communications ports to support communication on a process bus (IEC 61850-8-2 (GOOSE), IEC 61850-9-2LE (Sampled Values) and UCA2.0) AND station bus at the same time.		

		Communications card to support IEEE 1588 / Precision Time Protocol (PTP) to synchronize the test set to a PTP enabled Grandmaster clock in the substation for End-to-end tests / Synchro phasor tests. **Software** Download test templates created to the test set. Display all test set parameters. Create graphs from relay test results **General** Windows 10 (32bit and 64bit) software. Long filenames, tool tip help, context sensitive menu function (right mouse click) and an integrated help browser must be provided. All software functions, options and actions should be easily available by click of a button and/or shortcut key to avoid having to navigate through complicated menu structures and having to drill through multiple menu levels. The Microsoft ribbon based menu structure used in Office 365/ Office 2010 / Office 2013 is an example of sorting all functions, options and actions and making all easily accessible in the right place. No programming to be necessary to test an application - entry of setting parameters to be all that is required to set up and perform a test Future expansions in functionality by means of software updates. Firmware updating to be handled by the software, i.e. exchange of any hardware components is not permissible. Generation of reports on paper or file. All graphics and text to be printable.		

		Test report must be configurable to include custom information in graphical format (e.g. logos, wiring diagrams) and text format (tester, date of test, substation, reminders) The test set software should be able to import relay settings records from relay setting softwares (or relay settings databases) in a text file and/or XML format. An example is the XRIO file format for the transfer of relay setting parameters for all types of relays. It must be possible to test multi-function relays / panels with one test routine / document. Test sequence to be pausable at pre-defined points in the test sequence, by popping up a custom instruction dialogue (with or without audible warning), instructing the user to change either a setting on the relay, change of hard wiring or to record a specific measurement / status from the relay / scheme. All testing to be in closed loop. On Line Pass/ Fail assessment for ALL tests. This is particular important for automatic testing. Full automatic testing must be possible, i.e. without launching various test modules manually. Test software to be future proof to allow the complete testing of any new relay, both in form of entry of relay settings as well as testing all functions of such a relay. **Manual Control Function**		

		Direct entry of actual relay settings into test software. Test specific parameters (e.g. set I1 to 110% of I>>) must be automatically re-adjusted according to the entered relay settings. Manual and independent adjustment of amplitude, phase angle and frequency for all generator outputs. Graphical display of natural voltages and currents in a vector diagram. Direct entry sequence components and graphical display in a vector diagram Direct entry of impedances and graphical display in a R/X diagram Direct entry of power and graphical display in P/Q diagram Ramping and stepping of any of the above quantities: one, two or three phases at the same time Pulse ramping function for any of the above quantities Synchronized switching of multiple variables at the same instance in time. Timing function for pick-up and drop-out measurements. On-line Reporting Function Synchronise generator outputs to any third party signal, e.g. mains frequency. **State Sequencer Function** Direct entry of actual relay settings into test software. Test specific parameters (e.g. set I1 to 110% of I>>) must be automatically re-adjusted according to the entered relay settings.		

		Manual testing. Fully automatic testing. Ability to generate test sequences from any number of states. Each state consists of any combination of voltage, current, frequency and any binary output state. Graphical display of natural voltages and currents in a vector diagram. Graphical display of voltages, currents and binary signals over time. Direct entry sequence components and graphical display in a vector diagram Direct entry of impedances and graphical display in a R/X diagram Direct entry of power and graphical display in P/Q diagram Define trigger conditions for each state either in fixed time or dependent upon a logical combination of the binary inputs for accurate timing measurements. Synchronization to GPS / PTP and other digital timing pulses. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Linear Ramping and Pulse Ramping Functions** Direct entry of actual relay settings into test software. Test specific parameters (e.g. set I1 to 110% of I>>) must be automatically re-adjusted according to the entered relay settings. Manual testing.		

		Fully automatic testing. Ability to linearly ramp up to two independent variables (e.g. voltage and frequency) at the same time, while keeping the other quantities at a defined constant value. Ability to ramp a single variable in a pulsed fashion i.e. ramped quantity shall be set back to a predefined value between every step to allow the relay to reset. Graphical display of natural voltages and currents in a vector diagram. Graphical display of voltages, currents and binary signals over time. Define trigger conditions for pick-up / drop-out measurements upon a logical combination of the binary inputs. Synchronization to GPS / PTP and other digital timing pulses. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Transient Playback Function** IEEE COMTRADE (C37.111-1991) compatible (ASCII and binary formats). Synchronization to GPS / PTP and other digital timing pulses. Timing accuracy 1µs. Graphical display of voltage and current traces as well as digital signals and relay responses. Editing of signals: Extending (repeating) and deleting portions of the recorded signal.		

		Ability to generate composite harmonic wave shapes. Ability to edit own digital signals OR to select recorded binary signals to play back via binary outputs. Unlimited length (in time) of Comtrade file to play back. **Overcurrent Relay Testing** Direct entry of actual relay settings into test software. Test specific parameters (e.g. test points) must be automatically re-adjusted according to the entered relay settings. Manual testing. Fully automatic testing. Testing of the operating characteristic (triptime vs. Itest) for all types of fault (earth faults, phase faults, negative sequence and zero sequence faults) Display of overcurrent characteristic and testpoints in I/t diagram. Determination of the pick-up and drop-out current for all fault types. Determination the pick-up and drop-out of directional characteristic for directional overcurrent relays. Breaker simulation: Simulate the 52a and 52b auxiliary contacts of a breaker with the binary outputs, switch the currents off at zero crossing of current after a trip signal has been received. Characteristic formulae: IEC255-4, BS142 and IEEE PC37.112-1995, I2t characteristics to be supported. Definition of custom characteristics must be possible.		

		Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Frequency and Voltage Relays Testing** Direct entry of actual relay settings into test software. Test specific parameters (e.g. test points) must be automatically re-adjusted according to the entered relay settings. Manual testing. Fully automatic testing. Generation of ramps for amplitudes, phase angles and frequency. Pick-up, timing and stability tests. Graphic and tabular display of relay pick-up and drop-out vs. time. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Distance Relay Testing** Testing of line impedance relays and field failure characteristics on generator protection relays. Direct entry of the actual relay settings of an IED into the test software must automatically generate the trip characteristic. Any new differential algorithm that is introduced shall be accommodated in a reasonable time period; typically this should be at no cost to the purchaser.		

		Impedance characteristics supported: Quadrilateral, Mho, Tomato or Lens characteristic. Manual testing. Fully automatic testing. Test the trip time at specific fault impedances. Fault impedances must be specifiable in relation to zone reaches, e.g. 90% Z1. Automatic assessment of whether the tested trip time is passed or failed. Check the reach of a relay by placing a fault shot at the inner and outer tolerance border (i.e. theoretical reach minus and plus the defined reach tolerance of the relay). Automatic assessment of whether the tested reach is Passed or Failed. Automatically test the characteristic in the impedance plane (R/X diagram) and/or in the time grading diagram (Z/t diagram). Automatic assessment of whether the tested reach is passed or failed. Test models supported: constant test current, constant test voltage and constant source impedance. DC offset simulation: Control of angle of fault incidence, time constant of dc offset to be calculated on-line from system parameters (R/L). The separate arc resistance algorithm is to be supported for both earth faults and phase faults (as implemented on numerical distance relays). Apply Pre-fault voltage, i.e. duration settable.		

		Breaker simulation: Simulate the 52a and 52b auxiliary contacts of a breaker with the binary outputs, switch the currents off at zero crossing of current after a trip signal has been received. Graphical display of analog voltages and currents plus relay contact responses vs. time. Display of injected voltages and currents on a vector diagram in natural and/or in symmetrical components. Testing of auxiliary functions: Manual close, power swing, Auto-reclose function, VT fuse fail. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Differential Relay Testing** Testing of transformer differential relays, line differential relays, motor / generator differential and busbar differential relays. Direct entry of actual relay settings into test software. Test specific parameters (e.g. test points) must be automatically re-adjusted according to the entered relay settings. Manual testing. Fully automatic testing. Simulation of two and three winding transformers for all possible vector groups (e.g. YY0, YD1, YD11, etc.).		

		Testing the operating characteristic (Idiff vs. Ibias) for all types of fault: earth fault, phase faults and three phase faults. The various types of Ibias formulae (Ibias = (|Ip|+|Is|)/k; Ibias =max(Ip,Is); etc; numerical zero sequence elimination and both reference side have to be supported. Testing the harmonic restraint characteristic (Idiff vs. %I harmonic) for second harmonic (inrush restraint) and fifth harmonic (overfluxing restraint). Testing of the inrush restraint characteristic for relays which utilize the gap detection technique. Testing the trip time characteristic (triptime vs. Idiff) for all types of fault. Test the stability of the relay to confirm the correctness of the vector group correction, CT mismatch correction and zero sequence elimination. Apply pre-fault current, i.e. through fault current condition. Test current and duration settable. Apply voltage in addition to six currents - according to HV or LV voltage GPS / PTP synchronized End-to-end tests to test the operating characterisitic of line differential relays, i.e. by simulating either the local or remote end of a line. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal.		

		Synchronising Devices and Synchronising Check Devices Direct entry of actual relay settings into test software. Test specific parameters (e.g. test points) must be automatically re-adjusted according to the entered relay settings. Manual testing. Fully automatic testing. Adjustment control mode to be tested closed loop. Graphical of quantities in a DV/Df diagram as well as relative phase angles in a synchronoscope. Feedback signals: closing pulse and adjustment pulses (V+, V-, f+, f-). Display of adjustment controls vs. time. Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Testing with GOOSE Messages** All the above protective relay test modules must be support testing for IEC 61850-8-2, i.e. be able to trigger on GOOSE message as well as simulate GOOSE message (if need be). Import of SCD, ICD of GOOSE sniffer files to configure the triggering / simulation of GOOSE messages. Sensing of up to 360 simultaneous GOOSE message must be possible.		

		Testing with Sampled Values All the above protective relay test modules must be support testing for IEC 61850-9-2LE, i.e. be able to simulate all voltages and currents as Sampled Value signals. Simulation of up to THREE sets of IEC 61850-9-2LE signals must be possible. **Testing Single and Three Phase Transducers** Manual testing. Automatic testing. Generation of sweeps for amplitudes, phase angles, frequency, power (W, VA, Var). Individual setting of voltages and currents (ito amplitude, phase angle and frequency) should be possible per test point. For single phase transducers phase injected should be selectable. L-L voltage transducers to be tested with full three phase voltage system. On-line calculation of error (absolute, percentage and full-scale). Display of transducer output and absolute, percentage and full-scale errors vs. sweep quantity graphically and/or as table. Feedback signal: Low-level analog voltage (0..+/- 10V) or current (0..+/-20mA)		

		Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Energy meter testing according to IEC 62053** Manual testing. Automatic testing. Load test, No-load test, creep test, mechanism test, Injection test. Ability to generate automatic load profiles, i.e. test the meter for different load conditions. On-line display of error. "Feedback signal via optical pick-up / scanning head: Meter pulses of up to 100 KHz" Third Harmonic Test DC test Automatic assessment of test results (Pass / Fail) with actual results and deviation from nominal. **Power Quality Signal Generator** Testing of Power Quality measurement devices as per the standards to NRS 048-2 / SANS/IEC 61000-4-30; SANS/IEC 61000-4-7; BS EN/IEC 61000-4-15 and IEC 62586		

		On-line Multimeter Measurement Function Up to 10 independent inputs Software to display amplitudes and phase angles of ac voltages and currents, symmetrical components of voltages and currents, line to line voltages, frequencies, power (active, reactive and apparent) and cos(phi) independently for each input. All ac measurements to be true RMS. On-line vector diagram for voltages and currents as well as power. On-line measurement of Vdc, Idc and dc power for each input. On-line display of harmonics measured both numerically (in a table per harmonic frequency) and graphically in a bar graph. **Transient Recording Function** The recording function should enable recordings at the specified sampling frequency. Any recordings done, should automatically be uploaded to the controller PC. The recordings should be saved in COMTRADE format on the PCs harddrive. Trigger conditions: amplitude, swell and sag, harmonic, frequency, frequency change, notch as well as any combination of these triggers.		

		A pre-trigger buffer should be definable. Analysis software should be provided to analyze the recorded waveshapes in terms of analog waveshapes (RMS and instantaneous values), vector diagram, impedance plots (L-N and L-L fault loops) as well as harmonics. For impedance plots, the relays impedance characteristic should be displayable in relation to the impedance trajectory measured by the relay. **System-based testing of relays** Provide a power systems based simulation software to test the function of a relay in the primary power system, i.e. by specifying the primary power system topology (e.g. from a single line diagram) and power systems parameters (network source impedances, line and transformer impedances, CT and VT parameters). Allow definition of power system events, e.g. any type of fault (L-N, L-N, L-L-N and L-L-L faults) as well as switching event (opening of closing of breakers) Full Transient simulation simulating power system phenomena such as DC offset, saturation of CTs, power swings, series compensated lines, transformer in-rush, etc. Distributed testing, i.e. injecting multiple test set simulating voltages and currents and different points in the network from ONE PC - even when the test sets are physically NOT in the same location (e.g. at different ends of a line) **Special applications:**		

		Line impedance protection scheme on series compensated lines with tele-protection Line differential protection system testing (up to three terminals) Power transformer differential relay testing with internal winding faults Bus zone system testing for out-of-zone through faults, in-zone faults, dead-zone faults as well as isolator transition faults **ARC flash simulation** Facility to simulate an arc flash with a high intensity flash bulb inside medium voltage switchgear for trigger the arc flash sensor units of IEDs using both point sensors as well as linear fibre sensors. **Standard Accessories** Generator combination cable- to combine 3xV and 3xI into one test lead, 8x 4mm banana plugs Measurement leads-12 x 2 m; 2 mm^2 **Other accessories:** South African power cord Connection lead from test set to PC Various connection accessories		

		Hardcover carrying cases for test hardware (with retractable handle and wheels if available) Soft bag for test set and accessories Instruction Manual Software for report downloading and configuration as well as offline test sequence generation. **External integrated antenna PTP clock (GPS)** "The device should consist of a GNSS Antenna, GNSS Receiver and CPU combined in one unit and be weatherproof. The device should be easily setup. No RF cabling shall be required. The device should operate using an Ethernet cable up to 100 meters. The device should use extremely low power. To power up the device no additional power supply will be needed and should use powered over Ethernet (POE)." "Multiple time protocols shall be supported e.g. PTP and NTP timing networks. The device should fully support the following PTP profiles: IEEE 1588 default profile		

		IEEE C37.238-2011 IEEE C37.238-2017 IEC/IEEE 61850-9-3:2016" **MPTS hardware configurations** Calibration, Repair and Annual Reports **Guarantee** The unit and accessories shall be guaranteed for a period of at least 5 years. The guarantee may exclude any damage or failure that is deemed to have occurred due to negligence or abuse		
2.	**0664173**	**ANALYZER:SECONDARY INJECTION;0-100 PCT- GENERAL** A standalone and IED vendor independent PC software to examine IEC 61850 devices, unveil the inside view of any IEC 61850 IED, browsing and discovering the IED's data model, monitor IEC 61850 activity and work with SCL files **Standards** IEC 61850 Edition 1: Communication networks and systems in substations IEC 61850 Edition 2: Communication Networks and Systems in Power Systems IEC 61850-7-41-:2013 IEC 61850-7-420: 2009 IEC 61400-25-1:2006…-6:2010	**1**	

		Software Functions Universal client for IEC 61850 servers Unveil the inside of IEC 61850 devices via navigation view, detail view and an activity monitor Discover IEDs via sniffing function Import and analyse SCL files Save SCL files Browse IED according to the IEC 61850 data attributes Writing data and control structures Analysing Client/Server traffic on ACSI level Subscribe and simulate GOOSEs GOOSE sniffing Simulate IEC 61850 Ed.1 and Ed.2 IEDs **Minimum PC Requirements for Software** PC type- Windows 10 (32bit or 64bit), with the possibility to upgrade to Windows 11 RAM- A computer with x86 or x64 architecture Hard disk Space- 4GB Ethernet Port -2GB		

		Licensing mechanism- An Ethernet adapter with TCP/IP protocol bound to it, Software is licensed to the CPU via the internet. USB software dongles are NOT permissible. Transfer of licenses from one PC to another via internet. Local Support Hotline support hours- LOCALLY based engineers (i.e. from the Southern African region) are to present and demonstrate the software to our engineers at our companies substation on our own test application to prove its suitability to operate in local environmental conditions as well as to be able to test our test objects adequately. Email support- Local telephonic support to be offered in the hours of 07h00 - 20h00 South African time. Number of installations in operation in Southern Africa- Email support to be offered with a guaranteed turnaround time of 1 business day.		
3	**3000014012**	**TRAINING** Training – service Product training offered in South Africa off-site training to be offered on date of delivery Local off-site presentation and demonstration No Fitness For Duty, FFD required	**1**	

TOTAL PRICE EXC VAT	
TOTAL PRICE INCLUDING VAT	
Delivery cost	
Mode of Transport	
Lead times	
Payment terms	